

## Owicki-Gries:

S. Owicki and D. Gries:

An Axiomatic Proof Technique for Parallel Programs.  
Acta Informatica 6, 319-340 (1976).

Goal: Reason about parallel programs with shared variables.

Problem: Execution of thread 1 may change shared variables, and hence influence execution of thread 2.

Idea: (1) Limit influences by atomic (-)

(2) Ensure that influences do not invalidate a proof  
 $\Rightarrow$  Focus on proofs instead of fine-grained executions.

Approach:

- Prove each thread in isolation
- Show that commands of partner thread do not interfere with the proof
- Conclude correctness of the parallel program.

Technique: Extend Hoare's proof system by a rule for non-interference.

### Recall:

The rules of Hoare's proof system are

(SKIP)  $\frac{}{\{P\} \text{skip} \{P\}}$  (ASSIGN)  $\frac{}{\{P[E/x]\} x := E \{P\}}$

(ASSUME)  $\frac{}{\{P\} \text{assume}(B) \{P \wedge B\}}$

$\frac{\{P\} C_1 \{Q\}, \{Q\} C_2 \{R\}}{\{P\} C_1; C_2 \{R\}}$  (SEQ)

(CHOICE)  $\frac{\{P\} C_1 \{Q\}, \{P\} C_2 \{Q\}}{\{P\} C_1 \oplus C_2 \{Q\}}$

$\frac{\{P\} C \{P\}}{\{P\} C^* \{P\}}$  (LOOP)

(CONSEQ)  $\frac{P' \Rightarrow P, \{P\} C \{Q\}, Q \Rightarrow Q'}{\{P'\} C \{Q'\}}$

By HOARE  
we refer to these 7 rules.

## Interference Freedom

- We assume threads are annotated by a full proof,<sup>\*</sup> which means commands are interleaved with assertions

$$\{P_i\} C_i \{R_i\} C_2 \dots$$

Intuitively,  $P_i$  holds when the execution reaches  $C_i$ .

- In the presence of other threads, this intuition breaks:

$$\begin{array}{l} \{x=0\} \\ x := x+2; \\ \{x=2\} \end{array} \quad \text{and} \quad \begin{array}{l} \{x=0\} \\ x := 0; \\ \{\text{true}\} \end{array} \quad \text{hold}$$

but

$$\begin{array}{l} \{x=0\} \\ x := x+2; \parallel x := 0; \\ \{x=2\} \end{array} \quad \text{no longer holds.}$$

- The proofs do not take into account the interferences of the other threads.

### Definition:

- An assertion  $P$  is invariant under command  $T$  with precondition  $\text{pre}(T)$ , if

$$\{P \wedge \text{pre}(T)\} T \{P\}.$$

- Let  $C$  be a thread with full proof  $\{P\} C \{Q\}$ .  
Let  $T$  be a command from another thread with precondition  $\text{pre}(T)$  in the corresponding full proof.  
Then  $T$  does not interfere with proof  $\{P\} C \{Q\}$ ,

$$\text{if } \{R \wedge \text{pre}(T)\} T \{R\} \text{ holds}$$

for all assertions  $R$  in  $\{P\} C \{Q\}$  that are outside atomic blocks.

(also proof sketch, proof outline in the literature)

- Two full proofs  $\{P_1\} C_1 \{Q_1\}$  and  $\{P_2\} C_2 \{Q_2\}$  are interference-free, if every command/atomic block of the second thread does not interfere with the first proof and vice versa.

New proof rules

$$(OG-INTF) \frac{\{P_1\} C_1 \{Q_1\} \text{ the proofs are } \{P_2\} C_2 \{Q_2\} \text{ interference-free}}{\{P_1 \wedge P_2\} C_1 \parallel C_2 \{Q_1 \wedge Q_2\}} \quad \frac{\{P\} C \{Q\}}{\{P\} \text{atomic}(C) \{Q\}} (OG-ATOMIC)$$

Theorem:

$(OG-INTF)$  and  $(OG-ATOMIC)$  are sound.

Observation:

- Assume the length of thread  $C_i$  is  $l_i \in \mathbb{N}$ . Then these are 2:like interference-freeness proofs to do.
- Most of these proofs are trivial as the command or atomic block uses variables disjoint from the ones in the assertion.

Example:

- Consider program  $x := x + 2 \parallel x := 0$ .
- The full proofs
 

$\{x=0\}$	and	$\{true\}$
$x := x + 2$		$x := 0$
$\{x=2\}$		$\{x=0\}$

are correct, but not interference-free.

↳ As an example, consider

assertion  $\{x=0\}$  in thread 2.

Then

$\{x=0 \wedge x=0\} x := x + 2 \{x=0\}$  does not hold.



↳ Similarly

$\{x=2 \wedge \text{true}\} x:=0 \{x=2\}$  does not hold.

- We weaken the postconditions and consider the full proofs

$\{x=0\} x:=x+2 \{x=0 \vee x=2\}$  and  $\{\text{true}\} x:=0 \{x=0 \vee x=2\}$ .

These proofs are interference-free.

↳ For example does  $x:=x+2$  not interfere with another  $\{x=0 \vee x=2\}$  in the second proof, since

$\{(x=0 \vee x=2) \wedge x=0\} x:=x+2 \{x=0 \vee x=2\}$

holds.

↳ There are 4 such interference-freeness proofs to check.

- With rule (OG-INTF), we conclude

$\{x=0\} x:=x+2 \parallel x:=0 \{x=0 \vee x=2\}$ . □

## Auxiliary Variables

Rule (OG-INTF) alone is too weak to prove some programs.

### Lemma (Incompleteness)

$\{\text{true}\} x:=x+2 \parallel x:=0 \{x=0 \vee x=2\}$  cannot be derived in the proof system  $\text{HOARE} + (\text{OG-INTF}) + (\text{OG-ATOMIC})$ .

Proof:

Towards a contradiction, assume the triple could be proven in  $\text{HOARE} + (\text{OG-INTF})$ .

- Then there are full proofs

$$\{P_1\} x := x+2 \{Q_1\}$$

$$\{P_2\} x := 0 \{Q_2\}$$

that are interference-free  
and that satisfy

$$\text{true} \Rightarrow P_1 \wedge P_2 \quad (1)$$

$$Q_1 \wedge Q_2 \Rightarrow x=0 \vee x=2 \quad (2)$$

- From (1) we conclude

$$P_1 \Leftrightarrow \text{true} \text{ and } P_2 \Leftrightarrow \text{true.}$$

Hence, by (CONSEQ)

$$\{\text{true}\} x := x+2 \{Q_1\}.$$

This means

$$Q_1[x+2/x]$$

is valid (holds for all  $x \in \mathbb{Z}$ ).

Hence, also

$$Q_1$$

is valid. (3)

- We similarly derive

$$\{\text{true}\} x := 0 \{Q_2\}$$

from which we conclude validity of

$$Q_2[0/x].$$

(4)

- Using interference freedom, we conclude

for  $T = x := x+2$  with  $\text{pre}(T) = P_1 \Leftrightarrow \text{true}$

that

$$\{Q_2 \wedge \text{true}\} x := x+2 \{Q_2\} \text{ holds.}$$

This means

$$Q_2 \rightarrow Q_2[x+2/x] \text{ is valid.} \quad (5)$$

• Using induction, (4) and (5) show validity of  

$$\forall x: (x \geq 0 \wedge \text{even}(x) \Rightarrow Q_2) \quad (6)$$

• Since  $Q_1$  is valid by (3)  
 and  $Q_1 \wedge Q_2$  implies  $x=0 \vee x=2$  by (2),  
 (6) yields validity of

$$\forall x: (x \geq 0 \wedge \text{even}(x) \Rightarrow x=0 \vee x=2). \quad \Leftarrow \quad \square$$

- The problem is that we cannot conclude from  $x$  whether  $x := x+2$  has been executed.
- Trick: Introduce auxiliary variables.

### Definition:

Consider a program  $C$  and a set of variables  $\mathcal{A}$ .  
 Then  $\mathcal{A}$  is called a set of auxiliary variables for  $C$ ,

if every  $x \in \mathcal{A}$  only occurs in assignments  $z := t$  with  $z \in \mathcal{A}$ .

This means auxiliary variables

- $\hookrightarrow$  cannot influence the control flow of  $C$ ,  
 because they do not occur in conditions (assert).
- $\hookrightarrow$  cannot influence the data flow of  $C$ ,  
 because they cannot occur in assignments to variables outside  $\mathcal{A}$ .

### Example:

Consider program

$$z := x; (x := x+1 \parallel y := y+1).$$

Then the following are sets of auxiliary variables:

$$\emptyset, \{y\}, \{z\}, \{x, z\}, \{y, z\}, \{x, y, z\}.$$

Note that  $\{x\}$  is not a set of auxiliary variables

as

$$z := x.$$



We add a proof rule for introducing/eliminating auxiliary variables

$$(OG-INVX) \frac{\{P\} C \{Q\} \quad \exists \bar{A} \text{ a set of auxiliary variables for } C \\ \text{with } \bar{A} \cap (\text{fv}(P) \cup \text{fv}(Q)) = \emptyset}{\{P\} \text{ remove}(C, \bar{A}) \{Q\}}$$

Here,  $\text{remove}(C, \bar{A})$  is a program obtained from  $C$  by replacing all assignments  $x := t$  with  $x := \bar{A}$  by skip.

Theorem:

$(OG-INVX)$  is sound.

Example:

• Show

$$\{true\} x := x + 2 \parallel x := 0 \{x = 0 \vee x = 2\}$$

using auxiliary variable done.

Variable done indicates whether  $x := x + 2$  has been executed:

↳ initially false

↳ set to true atomically with  $x := x + 2$

• Show

$$\{true\} \left( \begin{array}{l} done := false; \\ \left( \begin{array}{l} \text{atomic} ( \\ x := x + 2; \\ done := true; \end{array} \parallel x := 0 \end{array} \right) \end{array} \right) \{x = 0 \vee x = 2\} \quad (7)$$

$$\{x = 0 \vee x = 2\}$$

With (7), we conclude

$$\{true\} \underbrace{\text{skip}; (\text{atomic}(x := x + 2; \text{skip}) \parallel x := 0)}_{\approx x := x + 2 \parallel x := 0} \{x = 0 \vee x = 2\}$$

with the observation that

$\{done\}$  is a set of auxiliary variables and  $(OG-INVX)$ .

• For the proof (7), note that

$$\begin{array}{l} \{\neg \text{done}\} \quad \text{and} \quad \{\text{true}\} \quad \text{hold.} \\ \text{atomic} ( \\ \quad x := x + 2; \\ \quad \text{done} := \text{true}; \\ \quad ) \\ \{\text{true}\} \end{array} \quad \begin{array}{l} x := 0 \\ \{(x = 0 \vee x = 2) \\ \wedge (\neg \text{done} \Rightarrow x = 0)\} \end{array}$$

These are full proofs (atomic sections do not need assertions) that are interference-free.

To show the latter, we check 4 conditions.

For example

$$\begin{array}{l} \{(x = 0 \vee x = 2) \wedge (\neg \text{done} \Rightarrow x = 0) \wedge \neg \text{done}\} \\ \{x = 0\} \\ \text{atomic} ( \\ \quad x := x + 2; \\ \quad \text{done} := \text{true}; \\ \quad ) \\ \{x = 2 \wedge \text{done}\} \\ \{(x = 0 \vee x = 2) \wedge (\neg \text{done} \Rightarrow x = 0)\}. \end{array}$$

• For the initialization, we have

$$\{\text{true}\} \text{ done} := \text{false} \{\neg \text{done}\}.$$

• We conclude with (SEQ). □