

Reversal-bounded Counter Machines

Definition (Counter machine)

- Let $C = \{c_1, \dots, c_n\}$ be a finite set of counters.
- A counter machine is a finite automaton $M = (\Sigma, Q, q_0, \rightarrow)$ // no final states with $\Sigma = \{inc, dec, zero\} \times C$.

- A configuration of M is a pair (q, v) with $v: C \rightarrow \mathbb{N}$. The initial configuration is $(q_0, 0)$.

- We have the following transition relation

$\rightarrow \subseteq (Q \times \mathbb{N}^C) \times (Q \times \mathbb{N}^C)$
between configurations:

$$(q, v) \rightarrow (q', v[c := c+1]), \text{ if } q \xrightarrow{inc(c)} q'$$

$$(q, v) \rightarrow (q', v[c := c-1]), \text{ if } q \xrightarrow{dec(c)} q' \text{ and } c > 0.$$

$$(q, v) \rightarrow (q', v), \text{ if } q \xrightarrow{zero(c)} q' \text{ and } c = 0.$$

Note:

Counter machines are Petri nets that can actively test for zero.

Theorem (Minsky '67)

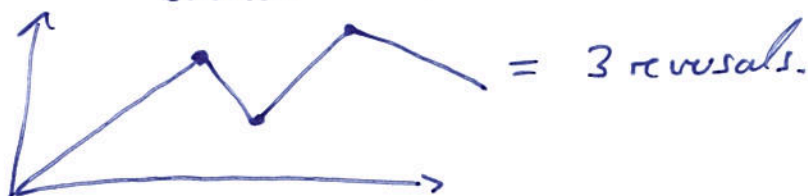
2-counter machines are Turing-complete.

Goal: Underapproximate behaviour of a counter machine

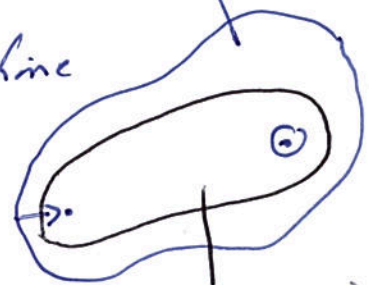
- ↳ Good for bug hunting in verification
- ↳ Habermehl et al. CAV '06:

Programs with lists are counter automata

Idea for underapproximation: Consider only reverse reversals between increment and decrement phases.

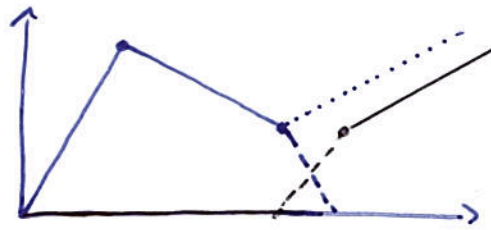


Space of reachable configurations (known as state space)



underapproximation

- Trick 1:
- Consider the 2-reversal behaviour of a 1-counter machine
 - We compute a new machine with 2-counters having 1-reversal each.



Technically: use states

$$Q \times \{pc, pd\} \times \{c_1, c_2\}$$

with the following meaning:

(q, pc, c_1) = increment phase of c_1 , no reversal happened so far

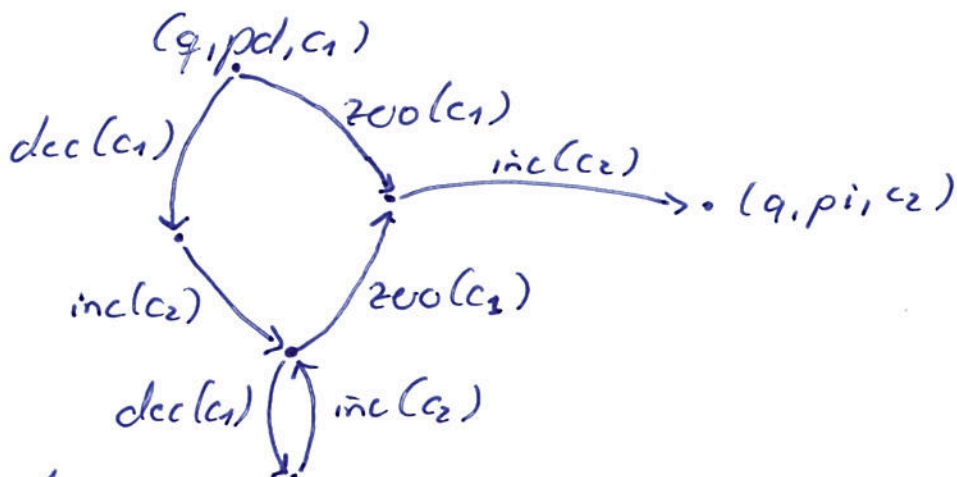
(q, pd, c_1) = decrement phase of c_1 , 1-reversal happened so far

(q, pc, c_2) = increment phase of c_2 , 2-reversals happened so far

↳ To mimic a transition

$$q \xrightarrow{\text{inc}(c)} q' \quad \text{in state } (q, pd, c_2),$$

use following gadget:



In general:

Theorem (Ibarra '79)

Consider an n -counter machine where all counters do r -reversals.

Then there is an $n \cdot (1 + \lfloor \frac{r}{2} \rfloor)$ -counter machine so that

$$(q_0, 0) \xrightarrow[r\text{-bounded}]{}^* (q, \bar{x}) \quad \text{iff} \quad (\tilde{q}_0, 0) \xrightarrow[1\text{-bounded}]{}^* (\tilde{q}, \bar{x}')$$

Indec, $x'(i, 1 + \lfloor \frac{r}{2} \rfloor) = x(i)$ for all $1 \leq i \leq n$.

Moreover, \tilde{q} contains p_i or p_d , depending on r .

Trick 2: Let M be an n -counter machine where all counters do \pm reversal.

Goal is to check reachability of (q, \bar{x}) .

The idea is to understand M as finite automaton with initial state q_0 and final state q .

Then

$L_{q_0, q}(M)$ (in $z00^* inc^* dec^* z00^*$)

is regular.

So $\mathcal{U}(L_{q_0, q}(M))$ is $\exists PIF$ -definable.

Moreover, the corresponding formula

$\mathcal{L}(x_{inc(c_1)}, x_{dec(c_1)}, \dots, x_{inc(c_n)}, x_{dec(c_n)})$

can be computed in linear-time.

The final configuration contains \bar{x} iff

$$x_c = x_{inc(c)} - x_{dec(c)}.$$

Theorem (Ibarra '78)

M reaches (q, \bar{x}) iff

$\exists x_{inc(c_1)}, x_{dec(c_1)}, \dots, x_{inc(c_n)}, x_{dec(c_n)}:$

$(\mathcal{L}(x_{inc(c_1)}, \dots, x_{dec(c_n)}) \wedge \bar{x} = \bar{x}_{inc} - \bar{x}_{dec})$

is satisfiable.